VASG Meeting
DAC 2003

Acting Chair: Steve Bailey
Minutes: John Willis, Gabe Moretti

IEEE Patent Policy read

FTL Systems noted that it has pending patents related to VHDL-200x. The presence of FTL Systems employees at VHDL-200x meetings does not imply license of any patent rights.

What is VHDL-200x?

The next language revision

Significant enhancements are planned

Focus is on:
• Performance and productivity
• Testbench & verification
• Modeling

One month after DAC, plan to have scope of fast track, intend to complete FT by end of 2003. VHPI LRM is in edit, expect to have LRM in ballot at end of year. Final 200x should be in ballot at end of 2004. DAC 2005 should consider next revision of VHDL.

Today:
Organized into teams
Prioritize enhancement requests

How does VHPI fit in?
LRM editing is funded
FT and VHPI both ready for ballot in December
   Ballot 2 PARs individually
   Ballot 2 PARs together
   Fold into 1 PAR and ballot together
   Recommend collapsing into one PAR.

Jim Lewis will coordinate with Paul to identify interactions between VHDLPLI and fast track.

Motion to thank Accelera for funding of VHPI LRM editing was made, seconded and passed by affirmation.

Top Priorities:
• Fast Track
• Modeling & Productivity
• Performance
• Assertions
• Modeling Enhancements
• Environment
• Generate Enhancements
• Testbench / verification
• Object-oriented enhancements
Direct C & Verilog
…
VHDL 200x-FT Fast Track
(from slides)
ISAC

VHDL 200x Modeling and Productivity

VHDL-200x Performance

VHDL Assertions
Alex will provide input from “VAL” and “E” efforts relating to assertions.

VHDL-200x Testbench and Verification

VHDL 200x Data Types and Abstraction
John will follow up with Paul related to generic types.

VHDL 2000x Environments
Need to include conditional compilation and Verilog inter-operability
MTI intends to donate related material
Kevin Cameron agreed to help with binary compatibility

VHDL 200x Asynchronous
Joshua to provide background tutorial

VHDL-PLI:
Deliverables: VHPI Integrated LRM and CDROM
Paul will investigate inclusion of CDROM in online archive.
John will cover license issues with Paul offline.

Next Meeting
Next meeting at FDL (Monday, 22 Sept)
Dennis, Ron, Steve and John need to find VHDL 1993 Principles document.

Attendance List:
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Gabe Moretti  gabe@eda.org
Kevin Cameron  dkc@grfx.com
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Others via Telecon:
John Shields
J. Bhasker
Jim Lewis
John Ries
Paul Menchini
(the above list is probably incomplete as I’m going by memory)