SV-EC Status
14 November 2003

David Smith – Chairman
Neil Korpusik – Co-Chairman
Agenda

• Mission
• Members
• Status
• Milestones
• Challenges
• SystemVerilog LRM - Status
Mission

- Maintain and extend the SystemVerilog language for testbench support
## Members

<table>
<thead>
<tr>
<th>Voting Company Members</th>
<th>Non-Voting Company Members (due to attendance)</th>
<th>Participating Individual Members</th>
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<tbody>
<tr>
<td>Arturo Salz (Synopsys)</td>
<td>Chris Spear (Synopsys)</td>
<td>Don Mills (LCDM)</td>
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<td>Brad Pierce (Synopsys)</td>
<td>Jeff Freedman (ModelTech)</td>
<td>James Young (HP)</td>
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<td>Cliff Cummings (Sunburst Design)</td>
<td>Stu Sutherland (Sutherland HDL)</td>
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<td>Dave Rich (Synopsys)</td>
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<td>David Smith (Synopsys)</td>
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<td>Dennis Brophy (ModelTech)</td>
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<td>Jay Lawrence (Cadence)</td>
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<td>Michael Burns (Motorola)</td>
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<td>Mehdi Mohtashemi (Synopsys)</td>
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<td>Neil Korpusik (Sun)</td>
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<td>Ray Ryan (ModelTech)</td>
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Status - Errata

- **Errata**
  - 43 items have been identified
  - 38 have been closed
  - 1 has been proposed
    - built-in global package
    - waiting for the Package and Separate Compilation proposal
  - 4 have been assigned and will be closed by the end of November (clarifications)
    - ERR-4: Lifetime of automatic variables in fork/join_*
    - ERR-8: Using Event Controls with Methods
    - ERR-20/21: Fix example based on ERR-4 resolution
Status - Extensions

- Extensions logged: 15
  - Moved:
    - EXT-6 Size of types (1 page)
    - EXT-13 to SV-BC general namespace support
  - Closed with no action:
    - EXT-5 Optional order of sign, etc...
  - Closed with no submission:
    - EXT-1 Additional string routines
    - EXT-4 Additional tasks for new types
  - Approved:
    - EXT-2 Fine Process Control (2 pages)
    - EXT-8 RandCase support (close to approval) (1 page)
    - EXT-11 Dynamic Queue Support (6 pages)
    - EXT-15 General foreach construct (1 page)
    - EXT-9 Stream Generation (7 pages)
    - EXT-10 Functional Coverage Specification (10 pages)
    - EXT-14 Constraint completion (11 pages)
  - Accepted:
    - EXT-7 Reacting to assertions (3 pages)
    - EXT-3 Virtual interfaces/ports (5 pages)
    - EXT-12 Bit stream support (pack/unpack) (6 pages)
Milestones

- **Completed:**
  - 7 July 2003 – Committees start operating
  - 4 August 2003 – Close of submission commitments
  - 15 September 2003 – Close of submissions

- **Upcoming:**
  - 1 December 2003
    - Complete technology errata
    - freeze technology of submissions
    - close implementation feedback

- **To come:**
  - 24 December 2003 – End of LRM development, start of LRM review
  - 24 January 2004 – Send to board
  - 24 February 2004 – Release 3.1a LRM
Challenges

• Keeping coordinated with other committees
Status – LRM Changes

• Draft 1 Complete on 29 October 2003
  – 37 Change requests
    • SV-AC: 2 change requests (formal semantics clean-up)
    • SV-BC: 16 change requests (BNF changes, both corrections and updates from 1364, and clean-up)
    • SV-CC: 3 change requests (BNF and Open array consolidation with dynamic arrays)
    • SV_EC: 16 change requests (Extensions plus clean-up)

• Draft 2 Planned for 1st week of December
  – http://www.eda.org/sv/LRMChanges_draft2.html
  – 19 change requests (so far)
    • SV-AC: 0
    • SV-BC: 4 (BNF corrections plus 1364 BNF updates)
    • SV-CC: 1 (correction)
    • SV-EC: 14 (Extensions plus corrections/clarifications from committee and Infineon review)