

Section 16.6.3, page 319 (IEEE Std 1800™-2009)

Replace

All operators that are valid for the types described in 16.6.1 are allowed with the exception of assignment operators and increment and decrement operators. SystemVerilog includes the C assignment operators, such as +=, and the C increment and decrement operators, ++ and --. These operators cannot be used in expressions that appear in assertions. This restriction prevents side effects.

With

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