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ELECTRONIC DESIGN PROCESSES (EDP) 2008 – CALL FOR PAPERS

APRIL 17-18th, 2008
MONTEREY BEACH HOTEL, MONTEREY, CALIFORNIA

The Electronic Design Processes (EDP) Workshop provides a forum for a cross-section of the design community to discuss state-of-the-art electronic design processes and CAD methodologies. The workshop focuses on the improvement of the overall design process, rather than on the functions of the individual tools themselves. Please visit <http://www.eda.org/edps> to see the list of past EDP speakers and presentations.

THEMES IN 2008: Power, Portability, and DFM

We solicit papers and proposals for special/panel sessions that shed light on the methodologies used for real current and future chip and system designs. Topics include but are not limited to:

- Methodology for the rapid design of embedded/portable systems
- Best practices and experiences with Power or DFM Methodology focus
- Multi-voltage Design and Power Management, Power Analysis
- Restrictive Design Rules (RDR) Impact on DFM
- Process/Device Characterization, Modeling implications
- Standards Activity: Helping or Hurting?
- Status of EDA Industry
- Multi-Core Programming/Implications
- Flow Integration, Scaling, and Migration
- Human Issues: Large & Distributed Teams, Offshoring, Training and Education
- Key Trends: Future Methodology Needs, Impact of Design Manufacturing Interface, Interoperability, Impact of the Web, Licensing Models, and Platforms

PAPER SUBMISSION

Authors should submit full-length, *original and unpublished* papers (maximum 20 pages in single-column double spaced format, or 6 pages in double-column conference proceedings format) along with author contact information. EDP is a workshop; the proceedings will not be formally published, and distribution is limited to attendees. Proposals for special and panel sessions may also be submitted; a 1-page description along with organizer contact information is required. Send Proposals via email to: edps@eda.org

IMPORTANT DATES

Submission Deadline: 29 February 2008

Acceptance Notification: 17 March 2008

Camera Ready Copy: 31 March 2007

On-site Registration: 16 April 2007

Organizing Committee:

General Chair: Patrick H. Madden

Technical Program Chair: Gary Smith

Steering Committee Chair: Bhanu Kapoor

Publicity Chair: Steve Grout

Preliminary Program Committee:

Michael Bohm (AccelChip)

Takahide Inoue (STARC)

Gabe Moretti (Gabe on EDA)

Sandeep Shukla (Virginia Tech)

Bhanu Kapoor (ArchPro)

Elaheh Bozorgzadeh (UCI)

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Gary Smith (GarySmithEDA)

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Juan-Antonio Carballo (Argon)

Dwight Hill (Synopsys)

John Lillis (UIC)

José Augusto Lima (U. of Minho)

Kumar Venkatramani (SoftJin)

Bill Halpin (Synplicity)

Dennis Sylvester (U. of Michigan)

Steven Levitan (U. of Pittsburgh)

Patrick Groeneveld (Magma)

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