

The Electronic Design Processes (EDP) Workshop provides a forum for a cross-section of the design community to discuss state-of-the-art electronic design processes and CAD methodologies. As the requirements and complexities of electronic design increase, past ad-hoc approaches to design processes are proving inadequate. The workshop focuses on the improvement of the overall design process, rather than on the functions of the individual tools themselves. The core audience of EDP consists of CAD system integrators and methodologists, academic researchers, and design team managers.

YET AGAIN

Focus in 2003: ^ it's the Methodology, Stupid!

We solicit papers and proposals for special/panel sessions that shed light on the methodologies used for real current and future chip and system designs. Methodology aspects of interest include but are not limited to:

- ❖ Best practices and experiences
- ❖ Domain-specific methodologies: SOC, analog / mixed-signal, RF
- ❖ Metrics
 - Cost, time-to-market, productivity
- ❖ Flow integration/optimization
- ❖ Scaling and migration
 - Scaling to diskbusters
 - Co-evolution of methodology and process technology
- ❖ Human issues
 - Large/distributed design teams
 - Training and education
- ❖ Manufacturing integration
 - Process/device characterization, modeling
- ❖ Domain-specific methodologies
 - Functional verification
 - RTL-down implementation
 - HW/SW co-design
 - IP reuse
- ❖ Key trends
 - Future methodology needs and concepts
 - Impact of design-manufacturing interface
 - Process advances and multi-technology integration
 - New tool/algorithms
 - Interoperability
 - Impact of the web, licensing models, and platforms

PAPER SUBMISSION

Unpublished workshop notes will be distributed to attendees. All papers must be submitted electronically through the website or by email to jantonio@us.ibm.com. Authors should submit full-length, *original and unpublished* papers (maximum 20 pages in single-column double spaced format, or 6 pages in double-column conference proceedings format) along with author contact information. Proposals for special and panel sessions may also be submitted; a 1-page description along with organizer contact information is required.

IMPORTANT DATES

Submission deadline	February 15, 2003
Acceptance notification	March 1, 2003
Camera-ready copy due	March 31, 2003
On-site registration	April 13, 2003

ORGANIZATION

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Gary Smith (Gartner)

Technical Program Chair

Juan-Antonio Carballo (IBM Research)

Publicity Chair

Steve Grout

Sponsors

IEEE DATC and CS, in cooperation with ACM/SIGDA.

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