



CALL FOR PAPERS

19th Annual Electronic Design Process Symposium

April 5 & 6, 2012
Monterey Beach Hotel, Monterey California

Sponsored by the
IEEE Computer Society Design Automation Technical Committee (DATC)
Electronics Design Process Subcommittee
and the
IEEE Computer Society of Silicon Valley
and the
Council on Electronic Design Automation (CEDA)

The Electronic Design Processes (EDP) 2011 Symposium provides a forum for a cross-section of the design community to discuss state-of-the-art electronics design processes and CAD methodologies. The Workshop focuses on the improvement of the overall design process, rather than on the functions of the individual tools themselves.

Visit <http://www.eda.org/edps> to see the list of past EDPS Workshop speakers and presentations.

Session Themes suggested:

- Parallel EDA
- High-Level Design - including Requirements-Driven Design Flows
- Cloud computing - including Software as a Service
- Low-Power Design - including Solution Mapping to ITRS Roadmap
- 3D IC's
- Automating Analog - What's there, what's missing?

Authors should submit full-length original and unpublished papers along with author contact information. Proposals for special, poster, and panel sessions may also be submitted; a 1-page description along with organizer contact information is required.

Send papers and Proposals to: edps@eda.org.

Important Dates:

- **Feb. 29 - Submission Deadline**
- **Mar. 16 - Acceptance Notification**
- **Mar. 30 - Camera Ready Copy**
- **Apr. 5 - On-site Registration**

Important Web Sites:

- www.eda.org/edps
- www.montereybeachresort.com

Subscribe for announcements and updates:
email edps-all@eda.org with "Subscribe" in the subject line.

Watch our site
for more
details.

A printable version of this EDPS 2012 Workshop Call For Papers (CFP) is here:
www.eda.org/edps/EDP2012/2012CFP.pdf